V_{CE} = 4500 V
I_C = 150 A

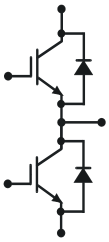


ABB HiPak

IGBT Module

5SNG 0150P450300

Doc. No. 5SYA 1593-05 02-2018

- **Ultra low loss, rugged SPT$^+$ chip-set**
- **Smooth switching SPT$^+$ chip-set for good EMC**
- **High insulation package**
- **AlSiC base-plate for high power cycling capability**
- **AlN substrate for low thermal resistance**
- **Recognized under UL1557, File E196689**

Maximum rated values [1)]

Parameter	Symbol	Conditions	min	max	Unit
Collector-emitter voltage	V_{CES}	V_{GE} = 0 V		4500	V
DC collector current	I_C	T_c = 80 °C		150	A
Peak collector current	I_{CM}	t_p = 1 ms, T_c = 80 °C		300	A
Gate-emitter voltage	V_{GES}		-20	20	V
Total power dissipation	P_{tot}	T_c = 25 °C, per switch (IGBT)		1450	W
DC forward current	I_F			150	A
Peak forward current	I_{FRM}			300	A
Surge current	I_{FSM}	V_R = 0 V, T_{vj} = 125 °C, t_p = 10 ms, half-sinewave		1400	A
IGBT short circuit SOA	t_{psc}	V_{CC} = 3400 V, $V_{CEM CHIP} \leq$ 4500 V $V_{GE} \leq$ 15 V, $T_{vj} \leq$ 125 °C		10	µs
Isolation voltage	V_{isol}	RMS, 1 min, f = 50 Hz		7400	V
Junction temperature	T_{vj}			125	°C
Junction operating temperature	$T_{vj(op)}$		-50	125	°C
Case temperature	T_c		-50	125	°C
Storage temperature	T_{stg}		-50	125	°C
Mounting torques [2)]	M_s	Base-heatsink, M6 screws	4	6	Nm
	M_{t1}	Main terminals, M6 screws	4	6	

[1)] Maximum rated values indicate limits beyond which damage to the device may occur per IEC 60747
[2)] For detailed mounting instructions refer to ABB Document No. 5SYA2039

IGBT characteristic values [3)]

Parameter	Symbol	Conditions		min	typ	max	Unit
Collector (-emitter) breakdown voltage	$V_{(BR)CES}$	V_{GE} = 0 V, I_C = 10 mA, T_{vj} = 25 °C		4500			V
Collector-emitter [4)] saturation voltage	$V_{CE\ sat}$	I_C = 150 A, V_{GE} = 15 V	T_{vj} = 25 °C		2.75		V
			T_{vj} = 125 °C		3.5		V
Collector cut-off current	I_{CES}	V_{CE} = 4500 V, V_{GE} = 0 V	T_{vj} = 25 °C			2	mA
			T_{vj} = 125 °C		6	20	mA
Gate leakage current	I_{GES}	V_{CE} = 0 V, V_{GE} = ±20 V, T_{vj} = 125 °C		-500		500	nA
Gate-emitter threshold voltage	$V_{GE(TO)}$	I_C = 40 mA, V_{CE} = V_{GE}, T_{vj} = 25 °C		5.5	6.2	7.5	V
Gate charge	Q_{ge}	I_C = 150 A, V_{CE} = 2800 V, V_{GE} = -15 V .. 15 V			1360		nC
Input capacitance	C_{ies}	V_{CE} = 25 V, V_{GE} = 0 V, f = 1 MHz, T_{vj} = 25 °C			17.5		nF
Output capacitance	C_{oes}				1.21		
Reverse transfer capacitance	C_{res}				0.32		
Internal gate resistance	R_{Gint}				3.1		Ω
Turn-on delay time	$t_{d(on)}$	V_{CC} = 2800 V, I_C = 150 A, R_G = 15 Ω, V_{GE} = ±15 V, L_σ = 400 nH, inductive load	T_{vj} = 25 °C		530		ns
			T_{vj} = 125 °C		510		
Rise time	t_r		T_{vj} = 25 °C		120		ns
			T_{vj} = 125 °C		130		
Turn-off delay time	$t_{d(off)}$	V_{CC} = 2800 V, I_C = 150 A, R_G = 15 Ω, V_{GE} = ±15 V, L_σ = 400 nH, inductive load	T_{vj} = 25 °C		1500		ns
			T_{vj} = 125 °C		1720		
Fall time	t_f		T_{vj} = 25 °C		400		ns
			T_{vj} = 125 °C		590		
Turn-on switching energy	E_{on}	V_{CC} = 2800 V, I_C = 150 A, V_{GE} = ±15 V, R_G = 15 Ω, L_σ = 400 nH, inductive load	T_{vj} = 25 °C		415		mJ
			T_{vj} = 125 °C		580		
Turn-off switching energy	E_{off}	V_{CC} = 2800 V, I_C = 150 A, V_{GE} = ±15 V, R_G = 15 Ω, L_σ = 400 nH, inductive load	T_{vj} = 25 °C		440		mJ
			T_{vj} = 125 °C		615		
Short circuit current	I_{SC}	t_{psc} ≤ 10 μs, V_{GE} = 15 V, T_{vj} = 125 °C, V_{CC} = 3400 V, $V_{CEM\ CHIP}$ ≤ 4500 V			660		A
Module stray inductance	$L_{\sigma\ DC}$	between C1 – E2			125		nH
Resistance, terminal-chip	$R_{CC'+EE'}$	between C1 – E2	T_C = 25 °C		0.78		mΩ
			T_C = 125 °C		1.03		

3) Characteristic values according to IEC 60747 – 9
4) Collector-emitter saturation voltage is given at chip level

Diode characteristic values [5]

Parameter	Symbol	Conditions		min	typ	max	Unit
Forward voltage [6]	V_F	I_F = 150 A	T_{vj} = 25 °C		3.2		V
			T_{vj} = 125 °C		3.45		
Reverse recovery current	I_{rr}	V_{CC} = 2800 V, I_F = 150 A, V_{GE} = ±15 V, R_G = 15 Ω L_σ = 400 nH inductive load	T_{vj} = 25 °C		270		A
			T_{vj} = 125 °C		305		
Recovered charge	Q_{rr}		T_{vj} = 25 °C		140		µC
			T_{vj} = 125 °C		235		
Reverse recovery time	t_{rr}		T_{vj} = 25 °C		780		ns
			T_{vj} = 125 °C		1330		
Reverse recovery energy	E_{rec}		T_{vj} = 25 °C		205		mJ
			T_{vj} = 125 °C		385		

[5] Characteristic values according to IEC 60747 – 2
[6] Forward voltage is given at chip level

Package properties [7]

Parameter	Symbol	Conditions	min	typ	max	Unit
IGBT thermal resistance junction to case	$R_{th(j-c)IGBT}$				0.062	K/W
Diode thermal resistance junction to case	$R_{th(j-c)DIODE}$				0.122	K/W
IGBT thermal resistance [2] case to heatsink	$R_{th(c-s)IGBT}$	IGBT per switch, λ grease = 1W/m × K		0.048		K/W
Diode thermal resistance [7] case to heatsink	$R_{th(c-s)DIODE}$	Diode per switch, λ grease = 1W/m × K		0.096		K/W
Partial discharge extinction voltage	V_e	f = 50 Hz, Q_{PD} ≤ 10pC (acc. to IEC 61287)	3500			V
Comparative tracking index	CTI			≥ 600		

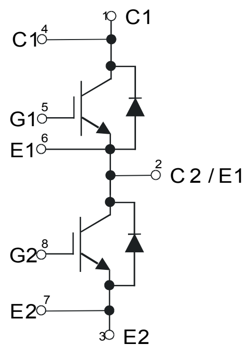
[2] For detailed mounting instructions refer to ABB Document No. 5SYA2039

Mechanical properties [7]

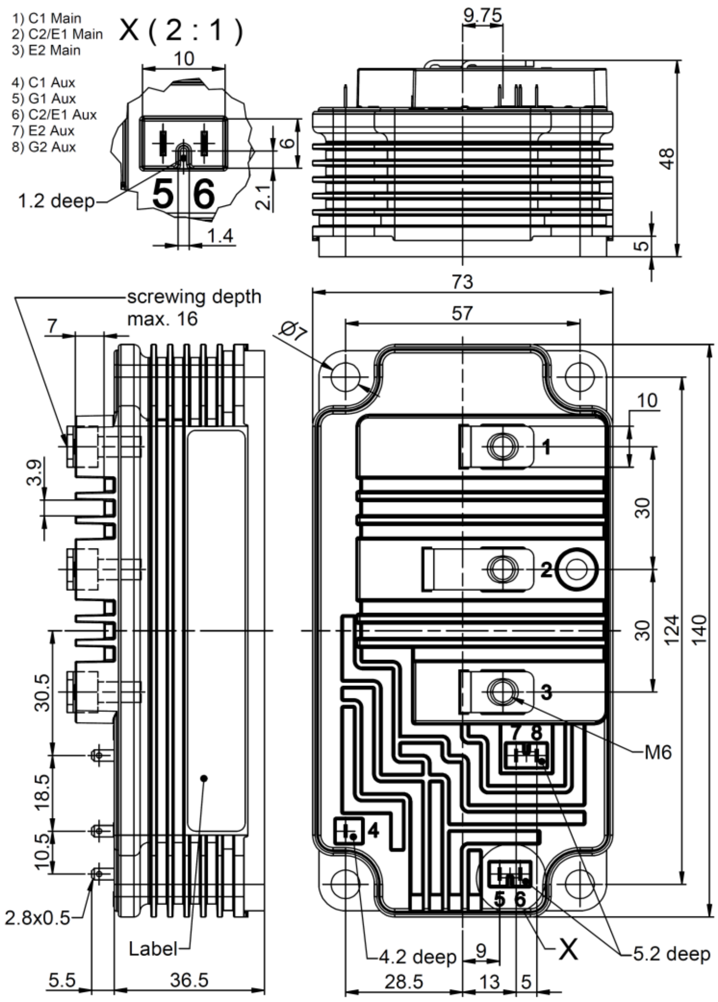
Parameter	Symbol	Conditions		min	typ	max	Unit
Dimensions	L × W × H	Typical , see outline drawing			73 × 140 × 48		mm
Clearance distance in air	d_a	according to IEC 60664-1 and EN 50124-1	Term. to base:	35			mm
			Term. to term:	19			
Surface creepage distance	d_s	according to IEC 60664-1 and EN 50124-1	Term. to base:	64			mm
			C1 to E1:	54			
			C1 to E2:	78			
Mass	m				460		g

[7] Package and mechanical properties according to IEC 60747 – 15

Electrical configuration



Outline drawing 2)



Note: all dimensions are shown in mm

2) For detailed mounting instructions refer to ABB Document No. 5SYA2039

This is an electrostatic sensitive device, please observe the international standard IEC 60747-1, chap. VIII.

This product has been designed and qualified for Industrial Level.

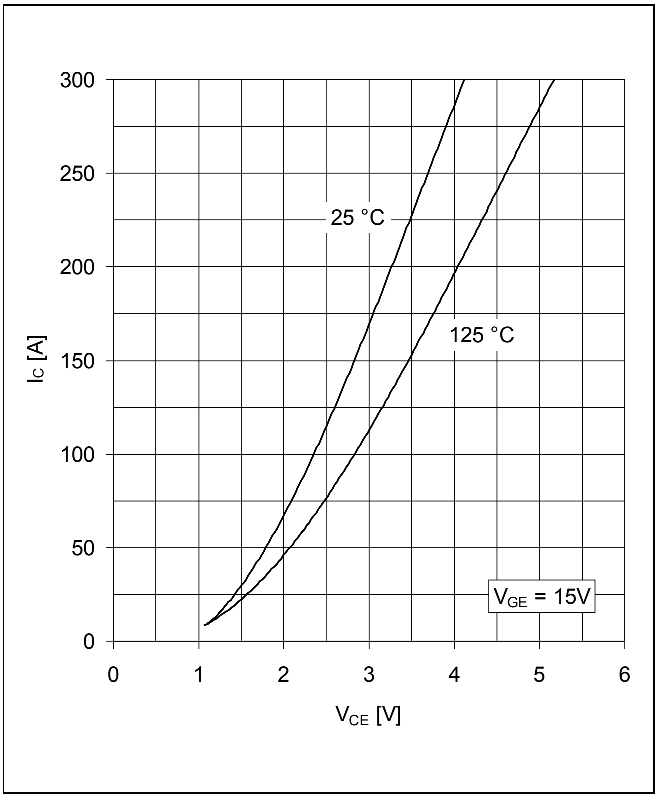


Fig. 1 Typical on-state characteristics, chip level

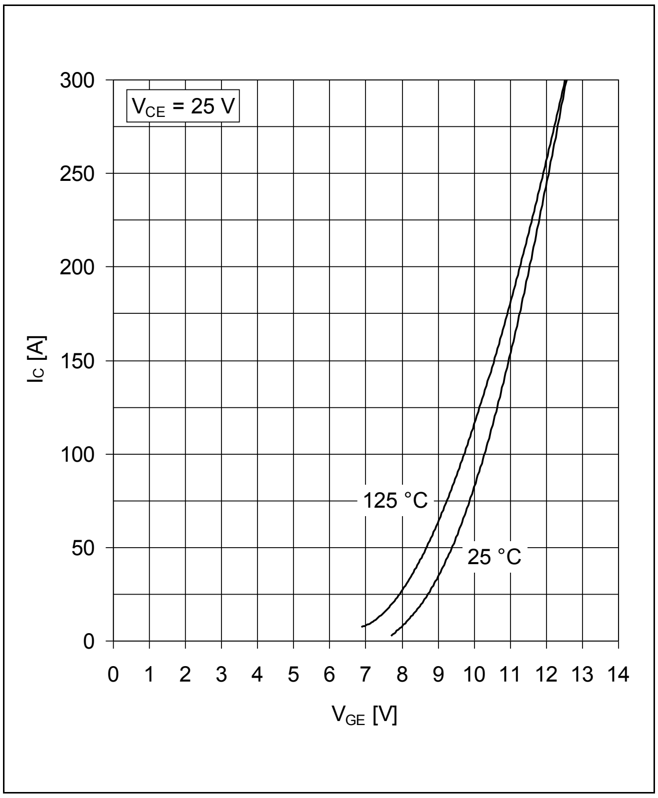


Fig. 2 Typical transfer characteristics, chip level

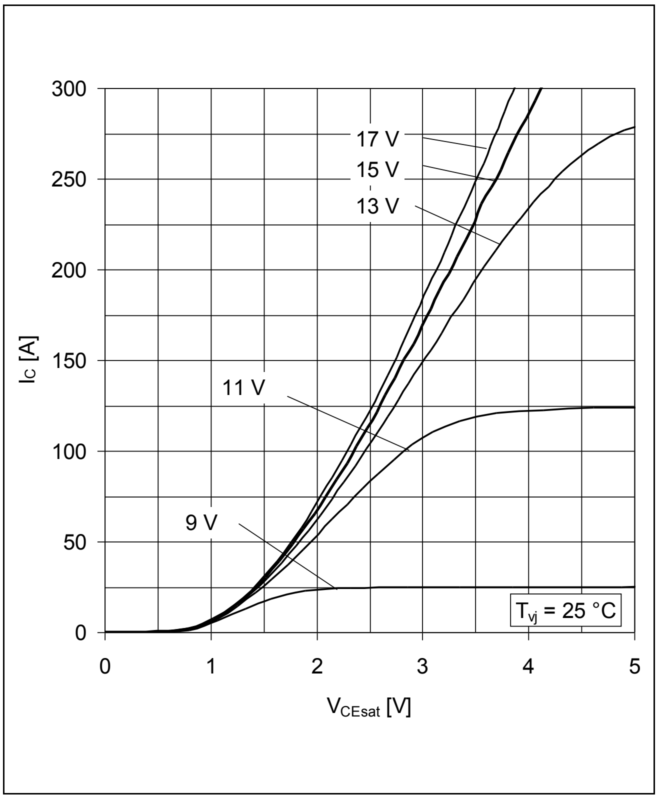


Fig. 3 Typical output characteristics, chip level

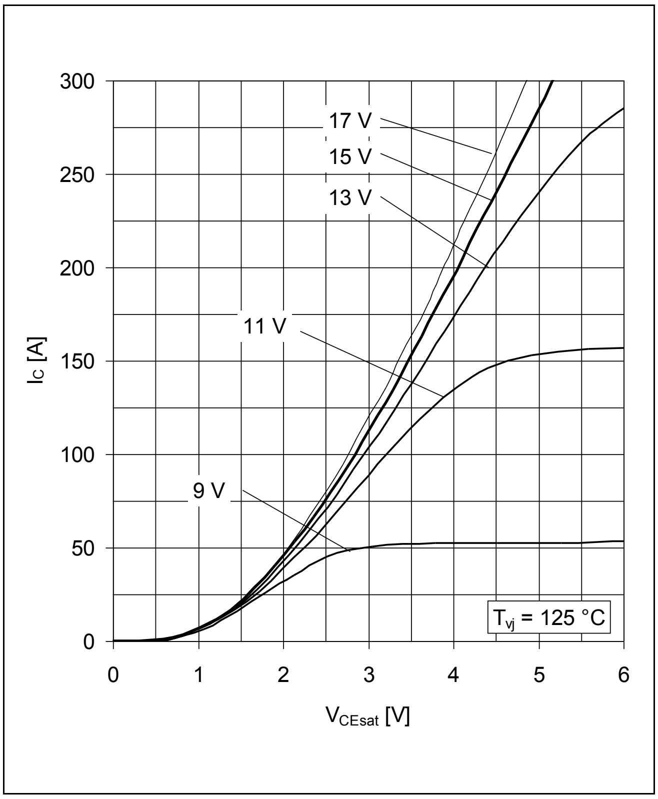


Fig. 4 Typical output characteristics, chip level

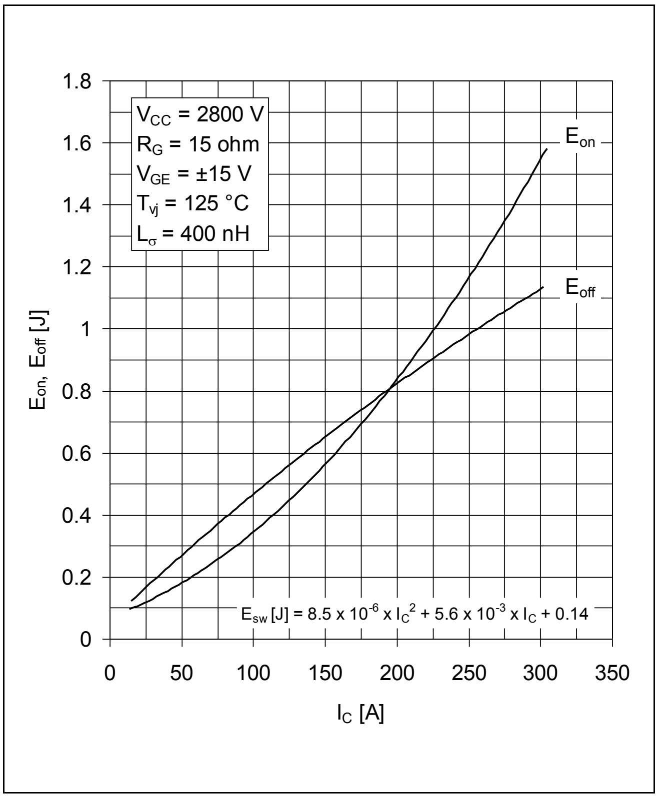


Fig. 5 Typical switching energies per pulse vs collector current

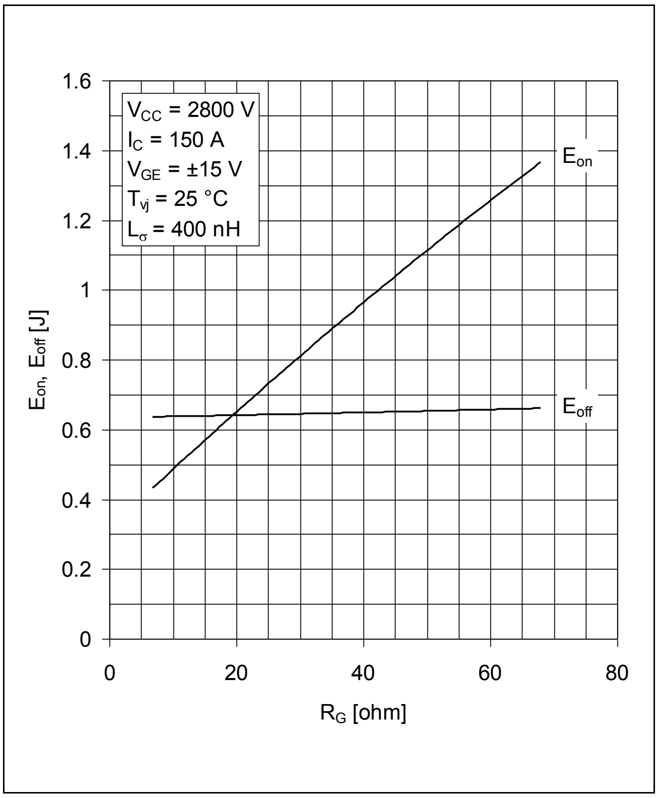


Fig. 6 Typical switching energies per pulse vs gate resistor

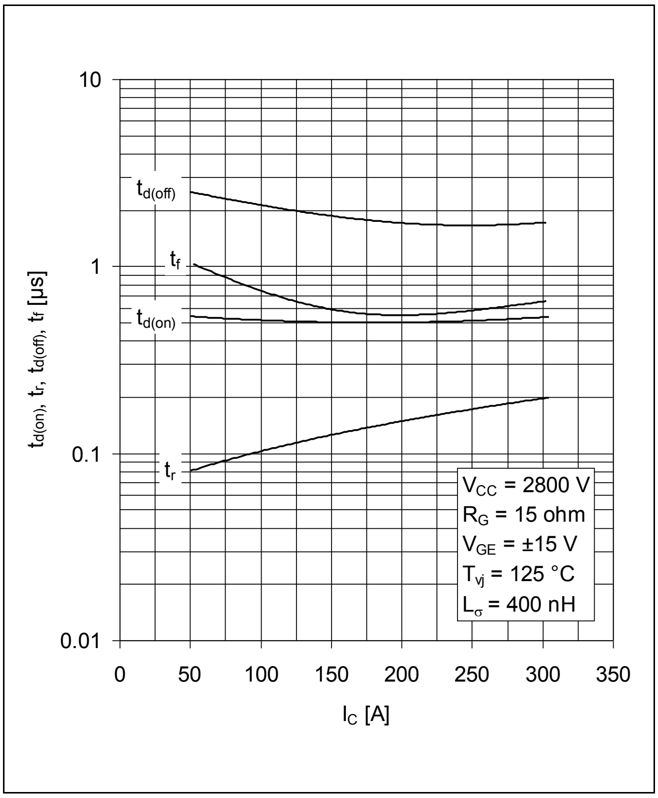


Fig. 7 Typical switching times vs collector current

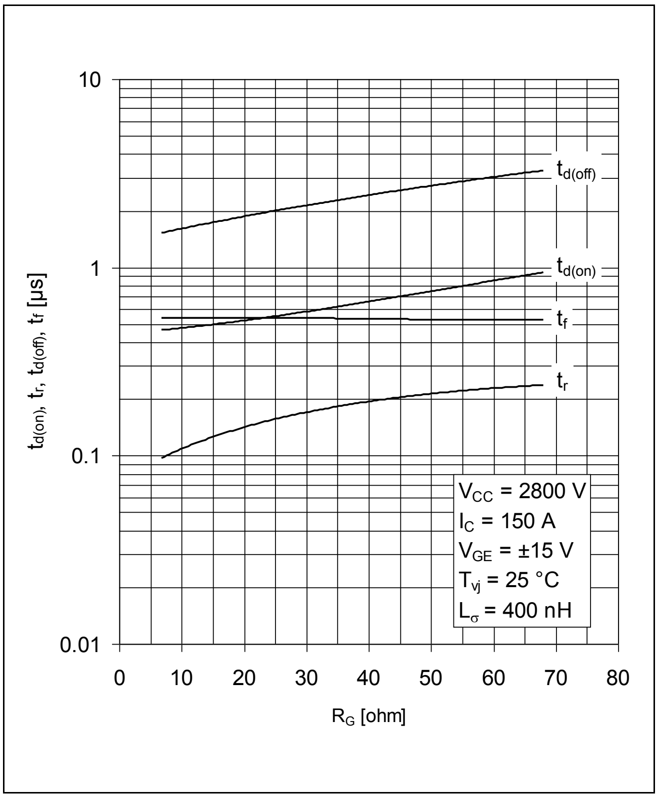


Fig. 8 Typical switching times vs gate resistor

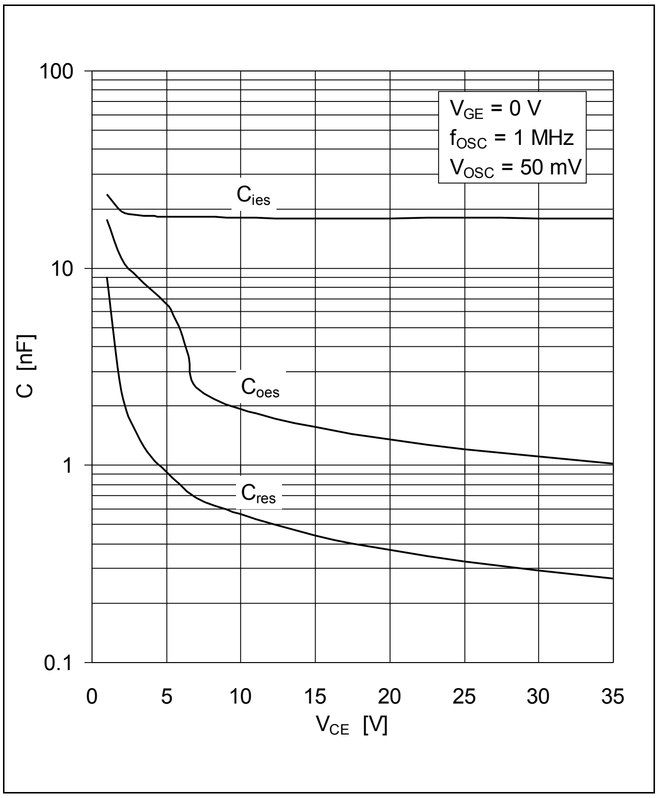


Fig. 9 Typical capacitances vs collector-emitter voltage

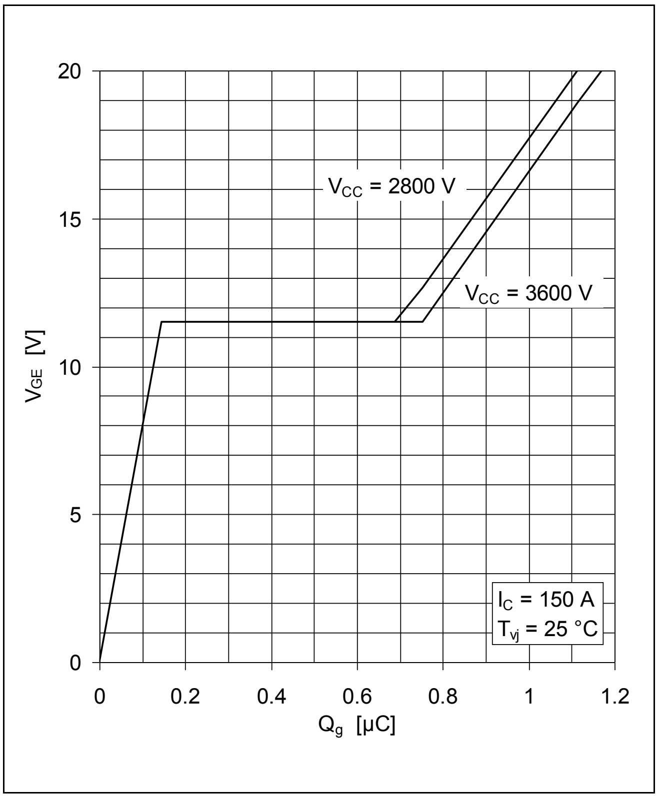


Fig. 10 Typical gate charge characteristics

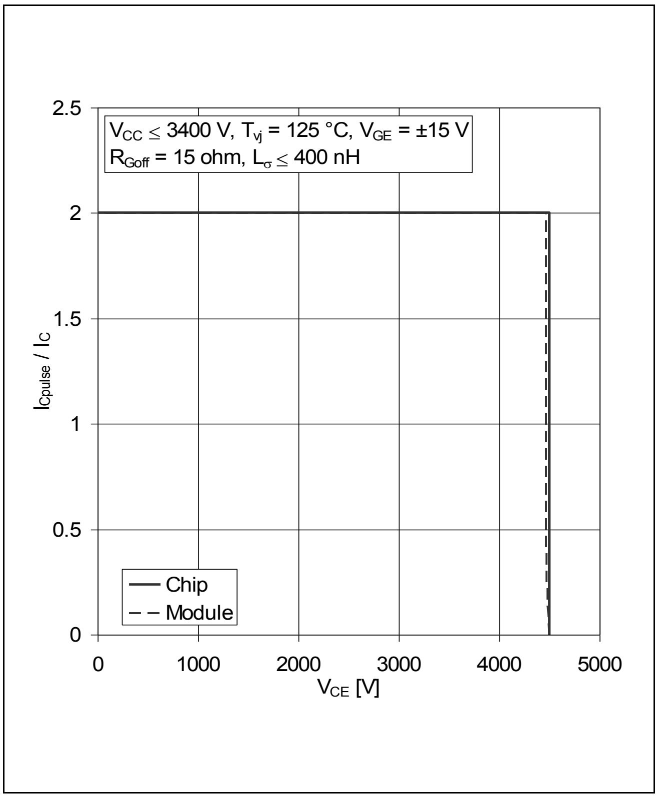


Fig. 11 Turn-off safe operating area (RBSOA)

ABB Switzerland Ltd, Semiconductors reserves the right to change specifications without notice.

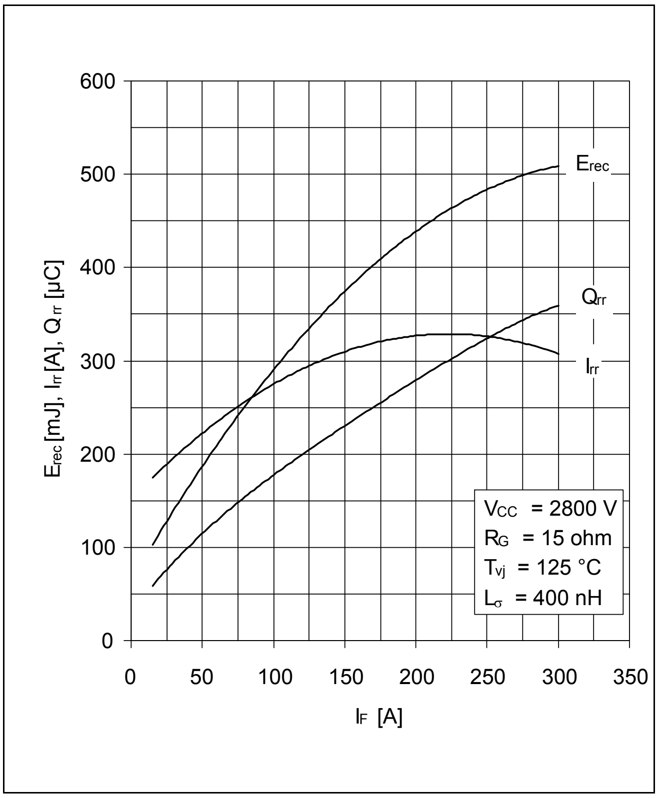


Fig. 12 Typical reverse recovery characteristics vs forward current

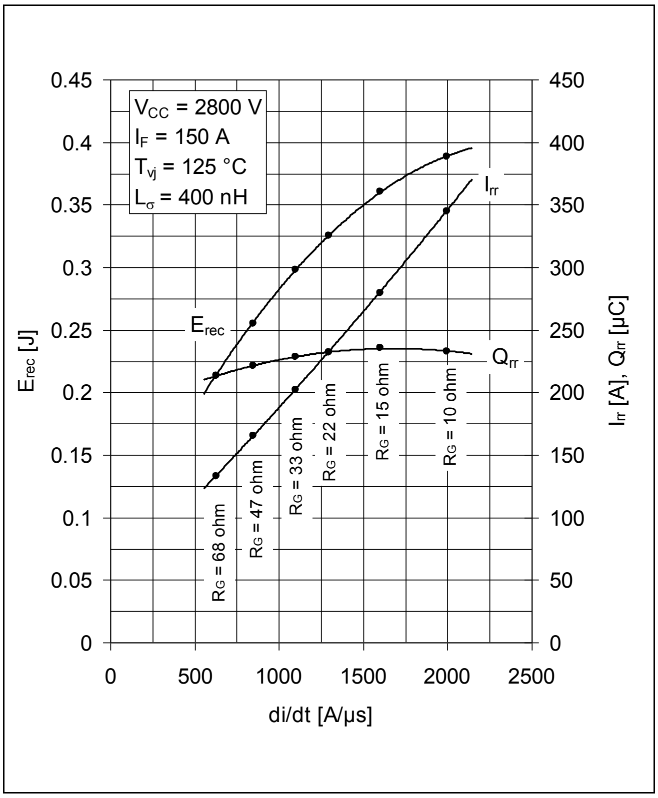


Fig. 13 Typical reverse recovery characteristics vs di/dt

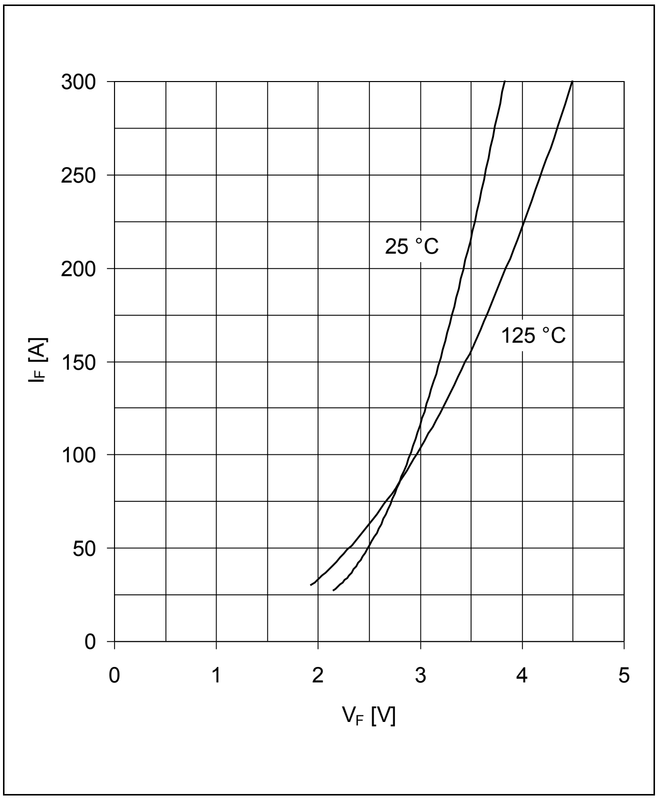


Fig. 14 Typical diode forward characteristics, chip level

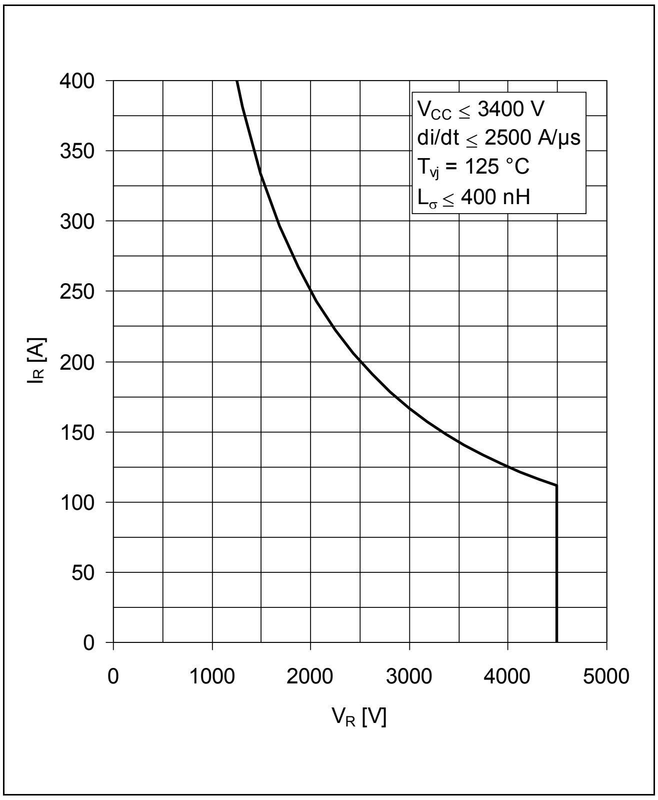


Fig. 15 Safe operating area diode (SOA)

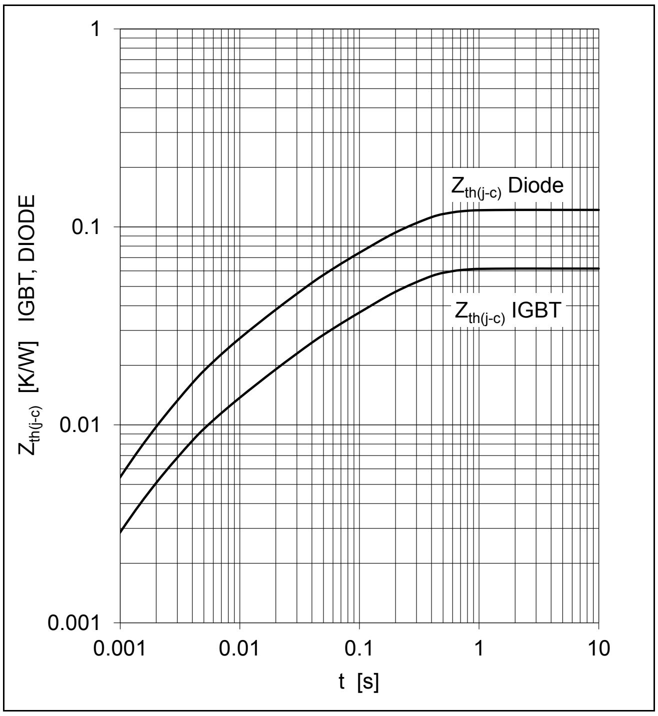


Fig. 16 Thermal impedance vs time

Analytical function for transient thermal impedance:

$$Z_{th\,(j\text{-}c)}(t) = \sum_{i=1}^{n} R_i (1 - e^{-t/\tau_i})$$

	i	1	2	3	4	5
IGBT	R_i(K/kW)	41.2	13.68	6.72		
	τ_i(ms)	192.6	21.4	2.78		
DIODE	R_i(K/kW)	80	28.04	13.84		
	τ_i(ms)	191.5	22.6	3.1		

For detailed information refer to:

- 5SYA 2039 Mounting instructions for HiPak modules
- 5SYA 2042 Failure rates of HiPak modules due to cosmic rays
- 5SYA 2043 Load – cycle capability of HiPaks
- 5SYA 2045 Thermal runaway during blocking
- 5SYA 2058 Surge currents for IGBT diodes
- 5SZK 9120 Specification of environmental class for HiPak

ABB Switzerland Ltd
Semiconductors
Fabrikstrasse 3
CH-5600 Lenzburg, Switzerland

Telephone +41 (0)58 586 1419
Fax +41 (0)58 586 1306
Email abbsem@ch.abb.com
Internet www.abb.com/semiconductors

Doc. No. 5SYA 1593-05 02-2018